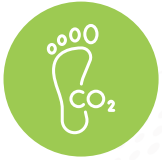


### Objectives



#### FASTLANE OBJECTIVE No. 1

##### Environmental footprint reduction

FASTLANE will demonstrate an overall environmental footprint reduction all along the product life cycle – thus contributing to the European Green Deal objective and Europe becoming the first Climate Neutral Continent by 2050.



#### FASTLANE OBJECTIVE No. 2

##### Sustainable European Sovereignty in Power Electronics

FASTLANE will secure European sovereignty in SiC-based power electronics by establishing a strategic value chain, boosting the EU's market position, and diversifying components. The project aims to increase production, reduce costs, and improve characterization processes for efficient SiC-based technologies.



#### FASTLANE OBJECTIVE No. 3

##### Broaden SiC functionalities

FASTLANE will broaden SiC functionalities by integrating on-die sensors to monitor the device temperature and current *in situ*, improving device, module and system protection and developing high performance high voltage solid-state circuit breaker.



#### FASTLANE OBJECTIVE No. 4

##### Overcome device-related limitations

FASTLANE will overcome device-related limitations thanks to advanced packaging, improved parasitics, and fusion of in-die current measurement with a Tunnel Magneto-Resistance (TMR) current sensor.



#### FASTLANE OBJECTIVE No. 5

##### Achieve cost benefits by reuse of automotive economy of scale

FASTLANE will demonstrate how cost benefits can be achieved by reusing the economy of scale of automotive power electronics for power conversion applications.

#### GENERAL INFORMATION:

Duration: 36 months

Start and end date: 2024 05 01 – 2027 04 30

Number of partners: 29

Total budget: € 96M

EU budget contribution: € 23M

## Partners



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